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Procedure For Calculation :- 1. Input Impedance A.
Connect A Decade Resistance Box (DRB) Between
Input Voltage Source And The Base Of The Transistor
(series Connection). B. Connect Ac Voltmeter
(0-100mV) Across The Biasing Resistor R 2. 10th, 2024

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On CRO Across The Load Resistor And Measure The
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In Figure 2.20. Show That The Algebraic Sum Of The

Currents Into This Portion Of The Network Must Be Zero. Solution: Prove: ` Use KCL At Node A (is A Fraction Of M` That flows To The Left At Node B): ` 2 6(` 12th, 2024

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