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DIGITAL SYSTEM DESIGN WITH VHDL AND FPGA

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MODULATION Muzakkir Mas'ud Adamu Department Of

Computer Engineering, Hussaini Adamu Federal

Polytechnic Kazaure, Jigawa State Nigeria. ABSTRACT:

A Pulse Width Modulation (PWM) Signal Controller Is

Implemented In A Digital Circuit To Control The Speed Of A DC Motor. May 1th, 2024
Digital System Design Using VHDL
Key Words: Digital Circuit, VHDL, Entity, Architecture. INTRODUCTION VHDL Means VHSIC Hardware Description Language Where VHSIC Stands For Very-High-Speed Integrated Circuit. VHDL Is A Hardware Description Language That Can Be Used To Model A Digital System .VHDL Is Commonly Used As A Design-entry Language For Field-programmable Gate Arrays
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Constructed Digital Circuit, VHDL Design Will Be Continued To Carry Out The System Implementation. Figure 4.
Tested Circuit Of Simple Stop Watch Circuit Using Two Displays Now, It Was Found To Be Explained That Is Why Needed To Use FPGA Based System On Chip Design. Following Section Will Be Presented VHDL Design With More Functions, More
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Begin 0 End Process Eqproc;
Eqproc:process (a, D, E)
Begin 0 End Process Eqproc;
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