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Digital Signal Processing Lab 2: Discrete Time Systems
Downsampling Taking One Sample Every M Samples Of A Given Sequence Is An Operation Called Decimation Of A Factor M. In Practice It Reduces The Sampling Frequency Of A Factor M (downsampling). 1) Consider The Sequence $X[n] = 3^n$, 2024.

Discrete-Time Signal Processing (DSP) Digital Signal Processing (Kuhn 2005) Analog/digital And

Digital/analog Converters, CPU, DSP, ASIC, FPGA

Advantages: $\frac{3}{4}$ noise Is Easy To Control After Initial Quantization $\frac{3}{4}$ highly Linear (with Limited Dynamic Range) $\frac{3}{4}$ complex Algorithms Fit Into A Single Chip

$\frac{3}{4}$ flexibility, Parameters Can Be Varied In Software 9th, 2024 Discrete Time Signal Processing Computer (Figure 1.1(b)). These Represent A Continuous-time, Discrete-time And Digital Signal Respec-tively. As We Know

From The Sampling Theorem, The Continuous-time Signal Can Be Reconstructed From Its Sam-ples Taken With A Sampling Rate At Least Twice The Highest Frequency Component In The Signal 25th, 2024 Signals

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