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Part I Consider Again The Four-bit Ripple-carry Adder Circuit In FigLu-e L. FA FA FA  
FA S I Figure L: A Four-bit Ripple Carry Adder. This Circuit Can Be Implemented  
Using A '+' Sign In VHDL. For Example, The Following Code Fragment Adds N-bit 2th,  
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Constant For Each Test Is Grayed, Pressure Was Held At 200 Torr For All Tests. Test  
1 Varied The Flow Of O2. Test 2 Varied The Flow Of Both O2 And SF6. Test 3 Varied  
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