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An Ultra-high-speed FPGA Based Digital Correlation Processor

An Ultra-high-speed FPGA Based Digital Correlation Processor Meteb M. Altaf 1,

Eball H. Ahmad , Wei Li2a), Houxiang Zhang2, Guoyuan Li2, And Changshun Yuan3 1 King Abdulaziz City For Science And Technology 2 Aalesund University College 3 Beihang University A) Windriver@126.com Abstract: This Paper Presents An Ultra-high-speed Correlation Processor For Mar 1th, 2024

High Speed, Digital To Analog Converters Basics

1 Introduction A Digital To Analog Converter (DAC Or D-to-A)is A Device That Converts Digital Codes To An Analog Signal. There Are Many Categories Of DACs. Some Of These Categories Include Sigma-deltaDACs, Pulse Width Modulators, Interpolating And High Speed DACs. In This Paper The Focus Will Be On The Interpolating And Feb 1th, 2024

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