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Condition Mainly Used With Binary Logic Elements Where The Logic State 1 (TRUE)

Is Converted To A Logic State 0 (FALSE) Or Vice Versa [IEC 60617-12, IEC 61082-2] 3.20 Logic Inversion Condition Mainly Used With Binary Logic Elements Where A Higher Physical Level Is Converted To A Lower Physical Level Or Vice Versa [Feb 9th, 2024

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Data0.mat Grid.mat Usage: The Show\_reachable.m Is The Program To Plot The Volumes Of Reachable Set At Different Altitudes And Speeds (as Shown In Fig.1). The Show\_target.m Is The Program To Plot The Target Set And Reachable Set At The Altitude H 0 = 1000m And The Speed V 0 = 180m/s Which Correspond To The Maximum Volume Of Reachable Set. The ... Apr 3th, 2024

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