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9 Chip Bonding At The First Level - The Chip CollectionOf Failure For An IC. 26% Of All IC Failures Are Related To The Wirebond. Figure 9-3 Shows The Fail-ure Mechanism Breakdown For Packaged Die. Chip Bonding At The First Level INTEGRATED CIRCUITENGINEERING CORPORATION 9-3 Source: ICE, "Roadmaps Of Packaging Technology" 22510 Wirebond TAB Flip Apr 11th, 2024"Checking Out" And "Checking In" Documents In SharePointThat Says "Others Cannot See Your Changes Until You Check In. Do You Want To Check In Now?" If You Are Not Done

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