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8-by-8 Bit Shift/Add Multiplier Giovanni D™ Aliesio 6 2 GENERAL REQUIREMENTS The Requirement Is To Design An 8-by-8 Bit Multiplier Based On The Shift And Add Method. The Overall Architecture Is Shown In Figure 2-1. The Multiplier Shall Accept As Inputs An 8-bit Multiplier And 8-bit Multiplicand As Well As A Start Signal. Apr 4th, 2024

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