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 Qualified As A Lawyer And Worked In The Banking Industry. He Spent 19 Years
 Closely Associated With NM From Whom He Received Naam Mantra In 1962. For The
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 May Neither Be In Series Nor In Parallel. Example 1.8 In The Circuit Shown In Figure
 1.11, The Voltage Source Is In Series With R_1 And The Current Source Is In Parallel
 With R_4 . None Of The Three Resistors Is Either In Series Or In Parallel With Any
 Other Resistor. R_1 R_2 R_3 Vs R_4 S Figure 1.11: 4th, 2024 Common Source Amplifier
 MOSFET Amplifier Distortion ECE315 / ECE515 MOSFET Amplifier Distortion (contd.) •
 Note For This Example, The DC Output Voltage Is The DC Drain Voltage, And That Its
 Value Is: $V_{OD} = V_{DD} - I_D R_D$ • Thus, The Total Output Voltage Is : $10.0 - 5.0 \cos \omega t$ V
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