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821-303-00 Reno, Nevada November 9, 20 Mar 4th, 2024.

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Power, Low-Cost Comparators With 2% Reference 3 PARAMETER Voltage Noise
C/E Temp. Ranges, IOUT = 17 mA MIN TYP MAXUNITS 100 Hz To 100 kHz 100 μ VRMS
CONDITIONS Output High Voltage V+ - 0.4 V V- + 0.4 V 15 25 Source Current µA
GND + 0.4 Output Low Voltage µA X 815 Feb 2th, 2024Ultra Low-Power, Single/Dual
Supply ComparatorsOrdering Information Continued At End Of Data Sheet. *Dice
Are Tested At $TA = +25$ °C, DC Parameters Only. **Contact Factory For Availability.
MAX921-MAX924 Ultra Low-Power, Single/Dual-Supply Comparators Maxim
Integrated Products 1 MAX921 OUT IN+ HYST REF V- GND V+ 21 4 5 6 8 3 7 IN-
THRESHOLD DETECTOR VINTypical Operating Circuit 19-0115; Rev 6; 4/09
PART TEMP RANGE PIN-PACKAGE Apr 8th, 2024.
INTRODUCTION TO CMOS OP-AMPS AND COMPARATORSJOHN WILEY & SONS, INC
8.2 Op-Amp Design Techniques And Examples / 316 8.3 Comparator Design
Techniques And Examples / 349 Problems / 355 References / 355 INDEX 357. Page
10 Blank. PREFACE Operational Amplifiers (op-amps) And Comparators Are Two Of
The Most Intricate, Jan 5th, 2024Single-Supply, High Speed PECL/LVPECL
Comparators DataOD = 250 MV , 1.3 V/ns , 500 MHz , $50\% \text{ Duty Cycle } 1.1 \text{ Ps Unit-}$
to-Unit Propagation Delay Skew 50 Ps POWER SUPPLY (ADCMP551/ADCMP552) Input
Supply Current I VCCI @ 3.3 V 8 12 17 MA Output Supply Current I VCCO @ 3.3 V
Without Load 3 5 9 MA Output Supply Current @ 3. Jan 16th, 2024Low Power Low
Offset Quad ComparatorsLow Power Low Offset Quad Comparators Document
Number: DS_P0000241 4 Version: B15 Application Description This Quad
Comparator Feature High Gain, Wide Bandwidth Characteristics. This Gives The
Device Oscillation Tendencies If The Outputs Are Ca Jan 7th, 2024.
Operational Amplifier Circuits Comparators And PositiveR P V O V V + Figure 16.
Non-inverting Schmitt Trigger The Voltage V + At The Non-inverting Terminal Is A
Combination Of The Output Voltage, V O, And The Input Voltage, Vin. $V + = Vin R 1$
R + 2 R 2 + V R 1 (1.10) O R 1 + R 2 Since V O = A V (+ -V -) The Transitions Will
Occur When V + Crosses Zero. 0 = Feb 13th, 2024

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