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3. BLOCK DIAGRAM AND SCHEMATIC DIAGRAMPDP-433PE, PDP-433PU Driver IC
Driver IC IC6501 Buffer IC Driver IC Driver IC Driver IC Driver IC Driver IC Driver IC
ADR CONNECT A ASSY AA1 Y4 Y1 Y2 D6 D18 D1 D8 D9 D16 D10 D11 D7 D4 D15
D14 D2 D17 D3 D12 D13 SW21 KL21 KL22 RE1 TE1 Y6 Y3 CLK/LE CLAMP A ASSY

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Amplifier Distortion (contd.) • Note For This Example, The DC Output Voltage Is The
DC Drain Voltage, And That Its Value Is: $V_{OD} V$ • Thus, The Total Output Voltage Is
: $10.0 \ 5.0 \ \cos \ 0 \ D \ O \ I \ V \ T \ V \ V \ T \ V \ \text{ĐW}$ It Is Very Important That You Realize There
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