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Chapter 8 Memory Hierarchy And Cache Memory

• Suppose Processor Has 2 Levels Of Hierarchy: Cache And Main Memory • $T_{\text{Cache}} = 1 \text{ Cycle}$, $T_{\text{MM}} = 100 \text{ Cycles}$ • What Is The AMAT Of The Program From Example 1? $\text{AMAT} = T_{\text{Cache}} + \text{MR Cache } (t_{\text{MM}}) = [1 + 0.375(100)] \text{ Cycles} = 38.5 \text{ Cycles}$ Memory Performance Example 2 1th, 2024

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The Memory/Storage Hierarchy And Virtual Memory

Storage Device Speed Vs. Size Facts: •CPU Needs Sub-nanosecond Access To Data To Run Instructions At Full Speed •Faststorage (sub-nanosecond) Is Small (100-1000 Bytes) •Big Storage (gigabytes) Is Slow (15 Nanoseconds) •Hugestorage (terabytes) Is Glaciallyslow (milliseconds) Goal: •Need Many Gigabytes Of Memory 2th, 2024

Memory Is A Constructive Process Memory - MCCC

Storage Involves Maintaining The Information Available In Memory - Whenever People Have Access To Information They No Longer Sense, Memory Is Involved 15 It's A Memory When... Example- If You Look Up A Phone Number, Go To The Telephone, And Dial The Number Then Memory Is Involved- Even If For Only Second 2th, 2024

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